

## Refine Search

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discard\$6 same circumnavigat\$6	3

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### Search History

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<u>L55</u>	discard\$6 same circumnavigat\$6	3	<u>L55</u>
<u>L54</u>	discard\$6 and 6490276.pn.	1	<u>L54</u>
<u>L53</u>	discard\$6 and L52	0	<u>L53</u>
<u>L52</u>	6496502.pn. and (database\$ or table\$ or list\$)	1	<u>L52</u>
<u>L51</u>	5938736.pn.	1	<u>L51</u>
<u>L50</u>	5938736.pn.	1	<u>L50</u>
<u>L49</u>	5949788.pn.	1	<u>L49</u>
<u>L48</u>	5949788.pn.	1	<u>L48</u>
<u>L47</u>	6128296.pn.	1	<u>L47</u>
<u>L46</u>	6128296.pn.	1	<u>L46</u>
<u>L45</u>	6266335.pn.	1	<u>L45</u>
<u>L44</u>	6496502.pn.	1	<u>L44</u>
<u>L43</u>	6496502.pn.	1	<u>L43</u>

<u>L42</u>	6636499.pn.	1	<u>L42</u>
<u>L41</u>	6636499.pn.	1	<u>L41</u>
<u>L40</u>	6654796.pn.	1	<u>L40</u>
<u>L39</u>	discard\$6 and l37	0	<u>L39</u>
<u>L38</u>	6654796.pn.	1	<u>L38</u>
<u>L37</u>	6490276.uref.	3	<u>L37</u>
<u>L36</u>	5953335.pn.	1	<u>L36</u>
<u>L35</u>	L34 and L32	1	<u>L35</u>
<u>L34</u>	L20 same (switch\$6 near1 module\$)	23	<u>L34</u>
<u>L33</u>	L24 and L32	7	<u>L33</u>
<u>L32</u>	forward\$6 near1 (database\$ or table\$ or list\$)	2441	<u>L32</u>
<u>L31</u>	(6490276.pn. or 6456597.pn. or 6233242.pn. or 5953335.pn.) and (database\$ or table\$ or list\$)	4	<u>L31</u>
<u>L30</u>	6490276.pn.	1	<u>L30</u>
<u>L29</u>	6490276.pn.	1	<u>L29</u>
<u>L28</u>	discard\$6 and L27	9	<u>L28</u>
<u>L27</u>	L21 and duplex	58	<u>L27</u>
<u>L26</u>	(hop\$ same discard\$6) and L25	2	<u>L26</u>
<u>L25</u>	L24 same (database\$ or table\$ or list\$)	25	<u>L25</u>
<u>L24</u>	L20 same port\$	170	<u>L24</u>
<u>L23</u>	L20 same port\$ same hop\$	1	<u>L23</u>
<u>L22</u>	L20 same port\$ same hop\$ same discard\$6	0	<u>L22</u>
<u>L21</u>	L20 same port\$	170	<u>L21</u>
<u>L20</u>	switch\$6 near1 network near1 system	1091	<u>L20</u>
<u>L19</u>	5274631.pn. or 5280480.pn. or 5088090.pn.	3	<u>L19</u>
<u>L18</u>	(database\$ or table\$ or list\$) and L17	7	<u>L18</u>
<u>L17</u>	L10 and (source adj1 port\$)	8	<u>L17</u>
<u>L16</u>	5442632.pn.	1	<u>L16</u>
<u>L15</u>	(L10 or L11) same discard\$6	6	<u>L15</u>
<u>L14</u>	((front adj1 panel\$) or front-panel\$) adj1 ports) same (ring near1 ports)	0	<u>L14</u>
<u>L13</u>	discard\$6 and L11	5	<u>L13</u>
<u>L12</u>	L10 and duplex	14	<u>L12</u>
<u>L11</u>	switch\$6 same (((front adj1 panel\$) or front-panel\$) adj1 ports)	10	<u>L11</u>
<u>L10</u>	switch\$6 same (ring near1 ports)	99	<u>L10</u>
<u>L9</u>	((front adj1 panel\$) or front-panel\$) same (ring near1 port)	0	<u>L9</u>
<u>L8</u>	(ring near1 ports) same switch\$6	99	<u>L8</u>
<u>L7</u>	L6 and (ring adj1 port\$)	9	<u>L7</u>
<u>L6</u>	port\$ near1 ASIC	293	<u>L6</u>
<u>L5</u>	(front adj1 panel\$ adj1 port\$) same (ring adj1 port\$)	1	<u>L5</u>
<u>L4</u>	(front adj1 panel\$) same (ring adj1 port\$)	10	<u>L4</u>
<u>L3</u>	L2 same hop\$	4	<u>L3</u>
<u>L2</u>	switch\$6 same (ring near1 port\$)	546	<u>L2</u>

L1 switch\$6 same ASIC same unit\$

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END OF SEARCH HISTORY